FEDEVEL

CERTIFICATION

This is to certify that

Jan Zygadlewicz

has successfully completed the course

Advanced Hardware Design

Date **10 SEP 2026**

Course tutor *Robert Feranec*

Robert Feranec

Course duration: 10h 50min **Software used:** Altium Designer **Course content:** 1) Starting a new project 2) Schematic design 3) Selecting components and creating libraries 4) Footprints, 3D models, starting a new PCB 5) Variants & BOMs 6) Checking libraries and schematic 7) PCB layout & Placement 8) PCB layout & Impedances, stackup 9) Stackup, finishing PCB, length matching 10) Generating output documentation

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Certificate number: 1c758a87800c3a9f4750078f4