FEDEVEL

# CERTIFICATION

This is to certify that

## Bob Vermeulen

has successfully completed the course

## Advanced PCB Layout

Date **23 SEP 2024**

Course tutor Robert Feranec

**Robert Feranec**

**Course duration:** 14h 55min **Software used:** Altium Designer **Course content:** 1) Placement 2) Basic Rules & Basic Stackup, Fanout under CPU, DDR3 preliminary layout 3) Board fanout, Planning Layout, Crosstalk, Impedances & Stackup 4) High Speed Design Rules & Routing, Layer ordering 5) Number of layers, Layout 6) Length matching, About memory routing, Polygons & Power Planes 7) Checking PCB, Length tuning, Finishing PCB, Generating & Checking gerber files

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**Certificate number:** 1c752a67509c6a3f6790977f9