FEDEVEL

CERTIFICATION

This is to certify that

Ing. Ondřej Veselka

has successfully completed the course

Advanced PCB Layout

Date **31 MAY 2024**

Course tutor Robert Feranec

Robert Feranec

Course duration: 14h 55min **Software used:** Altium Designer **Course content:** 1) Placement 2) Basic Rules & Basic Stackup, Fanout under CPU, DDR3 preliminary layout 3) Board fanout, Planning Layout, Crosstalk, Impedances & Stackup 4) High Speed Design Rules & Routing, Layer ordering 5) Number of layers, Layout 6) Length matching, About memory routing, Polygons & Power Planes 7) Checking PCB, Length tuning, Finishing PCB, Generating & Checking gerber files

www.fedevel.com

Certificate number: 1c751a67900c2a8f9720278f9