FEDEVEL ACADEMY

CERTIFICATION

This is to certify that

Renato Junior Ritter Veiga

has successfully completed a course

Advanced PCB Layout

Date: **18 JAN 2021 - 07 MAR 2021**

Course tutor Robert Feranec

Software used: **Altium Designer**
Course content: 1) Placement 2) Basic Rules & Basic Stackup, Fanout under CPU, DDR3 preliminary layout 3) Board fanout, Planning Layout, Crosstalk, Impedances & Stackup 4) High Speed Design Rules & Routing, Layer ordering 5) Number of layers, Layout 6) Length matching, About memory routing, Polygons & Power Planes 7) Checking PCB, Length tuning, Finishing PCB, Generating & Checking gerber files

Certificate number: 1c651a97200c7a3f1740370f7