

CERTIFICATION

This is to certify that

Amir Yeganeh-Khaksar

has successfully completed a course

Advanced PCB Layout

Date: 15 JUL 2019 - 1 SEP 2019

Course tutor

Robert Ferrier



Software used: **Altium Designer**

Course content: 1) Placement 2) Basic Rules & Basic Stackup, Fanout under CPU, DDR3 preliminary layout 3) Board fanout, Planning Layout, Crosstalk, Impedances & Stackup 4) High Speed Design Rules & Routing, Layer ordering 5) Number of layers, Layout 6) Length matching, About memory routing, Polygons & Power Planes 7) Checking PCB, Length tuning, Finishing PCB, Generating & Checking gerber files

Certificate number: 1d1485ffc10b10741080ead0174fe141