FEDEVEL

CERTIFICATION

This is to certify that

Jairo García González

has successfully completed the course

Advanced Digital Hardware Design

Date **14 JUN 2025**

Course tutor

Philip Salmony

Course duration: 11h 30min **Course content:** 1) System-Level Design 2) Part Selection 3) Schematic Fundamentals 4) PCB Design Fundamentals 5) Build-Up, Stack-Up, and Controlled Impedance 6) Power 7) FPGA/SoC Config & I/O 8) DDR3 Memory & Termination 9) Gigabit Ethernet 10) USB 2.0 HS & eMMC Memory 11) Final Touches & Manufacturing

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