

CERTIFICATION

This is to certify that

Bernardo Vicente López Villasana

has successfully completed
PHIL'S LAB course

Advanced Digital Hardware Design

Date: 27 NOV 2023

Course tutor:





Course duration: 11h 30min

Course content: 1) System-Level Design 2) Part Selection 3) Schematic Fundamentals 4) PCB Design Fundamentals 5) Build-Up, Stack-Up, and Controlled Impedance 6) Power 7) FPGA/SoC Config & I/O 8) DDR3 Memory & Termination 9) Gigabit Ethernet 10) USB 2.0 HS & eMMC Memory 11) Final Touches & Manufacturing

Certificate number: 136c958d27307c4557384e007